

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1999	327/206,208,210,541,543.ccls.	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:14
L2	130	326/23,24.ccls.	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:15
L3	1038	323/313,314.ccls.	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:15
L4	719	(1 or 2 or 3) and (parallel near5 transistor\$1)	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:28
L5	236	(1 or 2 or 3) and (parallel near5 (MOS or MOSFET\$1 or FET\$1))	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:30
L6	119	(1 or 2 or 3) and (parallel near5 (PMOS or NMOS))	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:16
L7	52	(1 or 2 or 3) and (parallel near5 (bipolar or PNP or NPN))	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:17
L8	97	(4 or 5 or 6 or 7) and hysteresis	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:17
L9	2533	(parallel near5 transistor\$1) same ((reference or constant) adj (voltage or potential))	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:05
L10	493	(parallel near5 transistor\$1) same (vref\$1 or "v.sub.ref" or vint\$1 or "v.sub.int")	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:06
L11	32	(parallel near5 transistor\$1) same ((internal or int) adj (voltage or potential))	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:07
L12	565	(parallel near5 (MOS or MOSFET\$1 or FET\$1)) same ((reference or constant) adj (voltage or potential))	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:33
L13	19	(parallel near5 (MOS or MOSFET\$1 or FET\$1)) same ((internal or int) adj (voltage or potential))	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:32
L14	106	(parallel near5 (MOS or MOSFET\$1 or FET\$1)) same (vref\$1 or "v.sub.ref"" or vint\$1 or ""v.sub.int")	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:32
L15	40	(parallel near5 (PMOS or NMOS)) same (vref\$1 or "v.sub.ref"" or vint\$1 or ""v.sub.int")	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:34
L16	5	(parallel near5 (PMOS or NMOS)) same ((internal or int) adj (voltage or potential))	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:33
L17	182	(parallel near5 (PMOS or NMOS)) same ((reference or constant) adj (voltage or potential))	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:33

L18	139	(parallel near5 (bipolar or PNP or NPN)) same ((reference or constant) adj (voltage or potential))	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:33
L19	1	(parallel near5 (bipolar or PNP or NPN)) same ((internal or int) adj (voltage or potential))	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:33
L20	18	(parallel near5 (bipolar or PNP or NPN)) same (vref\$1 or "v.sub.ref"" or vint\$1 or """v.sub.int")	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:34
L21	208	(1 or 2 or 3) and (9 or 10 or 11 or 12 or 13 or 14 or 15 or 16 or 17 or 18 or 19 or 20)	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:35
L22	190	21 not 8	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:35
L23	11	22 and 1 and (2 or 3)	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:40
L24	73	22 and (2 or 3) not 23	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:55
L25	106	22 not (23 or 24)	US-PGPUB; USPAT	OR	OFF	2005/03/23 13:55
L26	8034	(select\$3 or choos\$3) near5 ((reference or constant) adj (voltage or potential))	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:08
L27	1738	(select\$3 or choos\$3) near5 (vref\$1 or "v.sub.ref" or vint\$1 or "v.sub.int")	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:07
L28	216	(select\$3 or choos\$3) near5 ((internal or int) adj (voltage or potential))	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:07
L29	16	(mux or multiplex\$3 or multi-plex\$3) near5 ((internal or int) adj (voltage or potential))	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:07
L30	219	(mux or multiplex\$3 or multi-plex\$3) near5 (vref\$1 or "v.sub.ref" or vint\$1 or "v.sub.int")	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:08
L31	633	(mux or multiplex\$3 or multi-plex\$3) near5 ((reference or constant) adj (voltage or potential))	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:09
L32	72	(4 or 5 or 6) and (26 or 27 or 28 or 29 or 30 or 31)	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:16
L33	225	(1 or 2 or 3) and (26 or 27 or 28 or 29 or 30 or 31)	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:24
L34	153	33 not (8 or 21 or 32)	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:25
L35	34	34 and parallel	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:19

L36	119	34 not 35	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:19
L37	1231	"327"\$/ccls. and (26 or 27 or 28 or 29 or 30 or 31)	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:25
L38	1177	37 not (8 or 21 or 32)	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:25
L39	595	38 and parallel	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:25
L40	118	38 and hysteresis	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:26
L41	78	39 and 40	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:29
L42	40	40 not 41	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:30
L43	517	39 not 41	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:30
L44	509	43 and select\$3	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:31
L45	126	44 and ((second or 2nd or "2.sup.nd") adj reference)	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:44
L46	380	44 and reference not 45	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:45
L47	58	46 and PMOS and NMOS	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:47
L48	129	46 and (MOS or MOSFET) not 47	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:52
L49	58	46 and gate and drain and source not (47 or 48)	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:53
L50	135	46 not (47 or 48 or 49)	US-PGPUB; USPAT	OR	OFF	2005/03/23 14:53